

IN THE SPECIFICATION:

Please amend the specification as follows:

On Page 7, first paragraph:

In order to avoid this over weighting problem, decoders have been configured to round up all non-integer motion vectors to the nearest $\frac{1}{2}$ pixel. However, this was found not to be a good solution since it causes problems in the areas where there is no motion. In order to avoid these problems, the present invention is directed to MPEG decoding with embedded resizing that includes detecting and interpolating moving areas in interlaced video.

On Page 12, second paragraph:

One example of a pseudo-code for performing step 20 is shown in Figure 109. In the first line, a variable "diff" is initially set to zero. In the fourth line, the difference between the two fields of a data block is calculated. As can be seen, the difference between adjacent pixels $(j, j+1)$ of the two fields is calculated and accumulated for each column (i) of the data block selected in line 2. It should be noted that the difference taken is not the absolute difference. It should also be noted that the more columns (i) selected in line 2, a more accurate detection will be obtained. However, in order reduce

computations, a limited number of columns (i) may be selected in line 2. For example, only the first, last column, middle or every third column (i) may be selected to perform

On Page 13, third paragraph:

One example of a system in which the MPEG decoding that includes detecting and interpolating moving areas in interlaced video according to the present invention may be implemented is shown in Figure 11-10. By way of example, the system may represent a television, a set-top box, a desktop, laptop or palmtop computer, a personal digital assistant (PDA), a video/image storage device such as a video cassette recorder (VCR), a digital video recorder (DVR), a TiVO device, etc., as well as portions or combinations of these and other devices. The system includes one or more video sources